

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S58	2	"20040107449"	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 13:08
S59	2	"20040107449"	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 16:22
S60	0	("2004/0107449").URPN.	USPAT	OR	ON	2007/06/21 16:24
S50	228	S48 and @ad<"20031015"	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 16:34
S66	12	S65 and (@ad<"20031015" or @rlad<"20031015")	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 16:56
S65	12	("20010021317" "20010053513" "20020032706" "6037935" "6622306" "6675354" "6718515" "6785865").PN. OR ("6918090").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/21 16:56
S62	63	(portal or portlet) with focus	USPAT	OR	ON	2007/06/21 16:59
S61	0	(portal or portlet) with focus with style	USPAT	OR	ON	2007/06/21 16:59
S68	164	(portal or portlet) SAME (focus or unfocus)	USPAT	OR	ON	2007/06/21 17:00
S67	0	(portal or portlet) SAME focus SAME style	USPAT	OR	ON	2007/06/21 17:00
S64	4	S63 and (style adj sheet\$1)	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 17:00
S63	63	S62 and (@ad<"20031015" or @rlad<"20031015")	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 17:00
S71	30	S69 and style	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 17:01
S70	5	S69 and (style adj sheet\$1)	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 17:01

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S73	10	S72 and (@ad<"20031015" or @rlad<"20031015")	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 17:14
S72	10	("20010056485" "20020005866" "5559548" "5949955" "6052145" "6239794" "6268849" "6522342" "6557007" "6859907").PN. OR ("7174512"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/21 17:14
S69	160	S68 and (@ad<"20031015" or @rlad<"20031015")	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/21 17:14
L1	541	(715/523).CCLS.	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2007/06/23 16:47
S56	147	S55 and (@ad<"20031015" or @rlad<"20031015")	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/23 16:48
L2	434	L1 and (@ad<"20031015" or @rlad<"20031015")	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2007/06/23 16:48



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Best 200 shown

Relevance scale ☐ ☐ ☐ ☐ ☐21 [A study on the number of memory ports in multiple instruction issue machines](#)

Soo-Mook Moon, Kemal Ebcioglu

 December 1993 **Proceedings of the 26th annual international symposium on Microarchitecture MICRO 26**

Publisher: IEEE Computer Society Press

Full text available: pdf(1.28 MB) Additional Information: [full citation](#), [references](#), [citations](#)
Keywords: ILP, memory disambiguation, memory ports, speculative loads, static scheduling
22 [Increasing cache port efficiency for dynamic superscalar microprocessors](#)

Kenneth M. Wilson, Kunle Olukotun, Mendel Rosenblum

 May 1996 **ACM SIGARCH Computer Architecture News , Proceedings of the 23rd annual international symposium on Computer architecture ISCA '96**, Volume 24 Issue 2

Publisher: ACM Press

 Full text available: pdf(1.09 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The memory bandwidth demands of modern microprocessors require the use of a multi-ported cache to achieve peak performance. However, multi-ported caches are costly to implement. In this paper we propose techniques for improving the bandwidth of a single cache port by using additional buffering in the processor, and by taking maximum advantage of a wider cache port. We evaluate these techniques using realistic applications that include the operating system. Our techniques using a single-ported ca ...

23 [Synchronization for a multi-port frame buffer on a mesh-connected multicomputer](#)

Bin Wei, Gordon Stoll, Douglas W. Clark, Edward W. Felten, Kai Li, Patrick Hanrahan

 December 1995 **Proceedings of the IEEE symposium on Parallel rendering PRS '95**

Publisher: ACM Press

Full text available: pdf(856.27 KB) Additional Information: [full citation](#), [citations](#), [index terms](#)
Keywords: mesh routing network, multi-port frame buffer, multicomputers, parallel rendering, synchronization